



Substitute for form 1449A/BTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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<i>Complete if Known</i>	
Number	10/037,666
Inventor	January 3, 2002 Sriram Vajapeyam 2183
Name	David I. Huisman
cket Number	42390P11927

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

Examiner Signature	/David Huisman/	Date Considered	05/17/2006
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**Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.*

¹Applicant's unique citation designation number (optional). ²See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 801.04. ³Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

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Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
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of

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Complete If Known

Application Number	10/037,666
Filing Date	January 3, 2002
First Named Inventor	Sriram Vajapeyam
Art Unit	2183
Examiner Name	David J. Huisman
Attorney Docket Number	42390P11927

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.*	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ¹
DJH		Black, Bryan, et al., "The Block-based Trace Cache", Proceedings of the 26th International Symposium on Computer Architecture, pp. 196-207 (May 2-4, 1999)	
DJH		Smith, James E., et al., "Trace Processors: Moving to Fourth-Generation Microarchitectures", Computer, Vol. 30, Issue 9, pp. 68-74 (Sep. 1997).	
DJH		Vajapeyam, Sriram, et al., "Dynamic Vectorization: A Mechanism for Exploiting Far-Flung ILP in Ordinary Programs", Proceedings of the 26th International Symposium on Computer Architecture, pp. 16-27 (May 2-4, 1999)	
DJH		U.S. Application No. 10/950,693, entitled SYSTEM, METHOD AND APPARATUS FOR DEPENDENCY CHAIN PROCESSING, filed September 28, 2004, by Satish Narayanasamy et al.	

Examiner Signature	/David Huisman/	Date Considered	05/17/2006
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